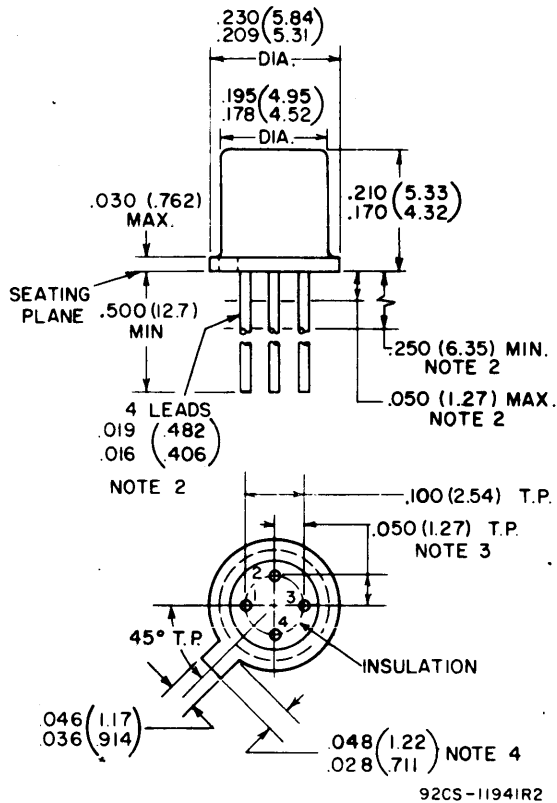


APPLICATION			REVISIONS						
QTY	MODEL USED ON	ASS'Y NO.	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD	APPD
6	HFRR-4 HFRR-5	AX5088 AX5008	Ø	ORIGINAL RELEASE	6/11/69	Ø	CV		JL



### SPECIFICATIONS

DRAIN CURRENT: .1 TO .3 mA  
 GATE VOLTAGE: -2V  
 "PINCH-OFF" CHARACTERISTICS  
 IN COMMON SOURCE CONFIGURATION

TX115 IS A JEDEC TYPE 3N128  
 TRANSISTOR WITH THE ABOVE  
 SPECS

### Dimensions in Inches and Millimeters

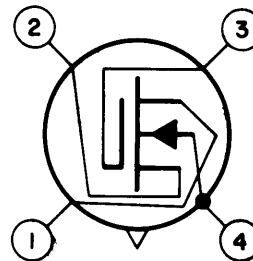
**Note 1:** Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated.

**Note 2:** The specified lead diameter applies in the zone between 0.050" (1.27 mm) and 0.250" (6.35 mm) from the seating plane. From 0.250" (6.35 mm) to the end of the lead a maximum diameter of 0.021" (0.533 mm) is held. Outside of these zones, the lead diameter is not controlled.

**Note 3:** Leads having a maximum diameter of 0.019" (0.482 mm) at a gauging plane of 0.054" (1.372 mm) + 0.001" (0.025 mm) - 0.000" (0.000 mm) below seating plane shall be within 0.007" (0.177 mm) at their true position (location) relative to a maximum width of tab.

**Note 4:** Measured from actual maximum diameter.

### TERMINAL DIAGRAM



- 1 - Drain
- 2 - Source
- 3 - Insulated Gate
- 4 - Bulk (Substrate) and Case

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYM.
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005					
FRACTIONS 1/64 ANGLES 0° - 30°					
MATERIAL		POSE		LIST OF MATERIAL	
FINISH		APPROVAL	DATE 6/11/69	THE TECHNICAL MATERIEL CORP.	
		MECH. DES.	DATE	MAMARONECK, NEW YORK	
		ELECT. DES.	DATE	TRANSISTOR, SILICON	
		CHECKED	DATE 5/27/69	(3N128)	
S401-327(3N128)		DRAWN	DATE 5/22/69		
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				DWG NO. TX 115	ISSUE Ø
SCALE				SHEET	OF