



# TMC SPECIFICATION

NO. S1398

REV: A

COMPILED:

CHECKED:

APPD:

SHEET 2 OF

TITLE:

ACCEPTANCE TEST PROCEDURE FOR 4079992-1 (A5793 SIDEBAND ASSEMBLY)  
4079993-2 (A5795 FSK ASSEMBLY)  
4079994-1 (A5794 CARRIER GEN. ASSEMBLY)

## I INTRODUCTION

This test set is used to test the various boards required. It is designed to operate with either an external or an optional internal 1 MHz standard. A switch is provided at the rear of the test set to properly switch in the desired standard. If an external 1 MHz standard is used its amplitude should be at least 1V<sub>P.P.</sub>. If an internal 1 MHz standard is used a +12V @ 1A. DC power source must be connected to the NF120 +12V terminals at the rear of the test set. At least 30 minutes warm up time must be given for the proper operation of the internal 1 MHz standard.

NOTE: Record data and results of tests on the test data sheet provided. If there are any failures record them on the log of failure sheet. Also record the corrective action taken if any.

## II TEST EQUIPMENT REQUIRED

NOTE: All test equipment must be calibrated. Record Serial numbers and Cal. dates on the appropriate test data sheets.

- A. OSCILLOSCOPE - H.P. Type 541 or Equivalent
- B. +12VD.C. POWER SUPPLIES AS REQUIRED
- C. +24VD.C. POWER SUPPLY (FS ONLY)
- D. FREQUENCY COUNTER - H.P. 5244L or Equivalent
- E. AUDIO GENERATOR - H.P. 200CD or Equivalent
- F. VTVM - Ballantine 314 or Equivalent
- G. AX5253 - LOCKHEED TEST SET
- H. H P 141 T Spectrum Analyzer with 8552B Head or Equivalent.



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2. VERIFY LEVEL IS ADJUSTABLE TO 50MVP.P. (ADJUSTED BY R47)
  3. READJUST TO 30<sup>MV</sup>P.P. (TRUE LEVEL AS DESIRED IS DETERMINED IN ACTUAL SYSTEM). RECORD RESULTS ON DATA SHEET.
- E. Set MODE control to CW and insert a key plug into the KEY jack.
- F. Set the MON CNTL to OUT 1. There should be no signal present on scope until the key line is grounded.
- G. Ground the key line (Key plug switch ON) and check for the following:
1. FREQUENCY - 250 KHZ
  2. VERIFY LEVEL IS ADJUSTABLE TO 50 M<sup>V</sup>P.P. (adjusted by R20)
  3. READJUST TO 30 M<sup>V</sup>P.P. (TRUE LEVEL AS DESIRED IS DETERMINED IN ACTUAL SYSTEM) RECORD RESULTS ON TEST DATA SHEET.
- H. Set the MON CNTL to OUT 2. There should be no signal present on scope until the key line is grounded (Key plug switch ON).
- I. With the key line grounded check for the following:
1. FREQUENCY - 250 KHZ
  2. VERIFY LEVEL IS ADJUSTABLE TO 50M<sup>V</sup>P.P. (adjusted by R27)
  3. READJUST TO 30MVP.P. (TRUE LEVEL AS DESIRED IS DETERMINED IN ACTUAL SYSTEM) RECORD RESULTS ON TEST DATA SHEET.
- J. Set MODE control to AM.
- K. Apply a 1 KHZ audio signal to AUDIO IN terminals at rear of test set. Set the audio level from audio generator to read 10 M<sup>V</sup> on the VTVM. "
- L. Set the MON CNTL to AM and check for the following:
1. FREQUENCY - Amplitude Modulated signal of 250 KHZ & 1 KHZ
  2. LEVEL Approximately 30 mv (adjusted by R69) Record results (TRUE LEVEL AS DESIRED IS DETERMINED IN ACTUAL SYSTEM)





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S. Test for frequency response (Ripple) Test on Sideband  
Assembly A5793 4079992-1.

Set audio generator to obtain reference level on output  
(Analyzer). Record db levels within Bandpass limits of  
filters. The ripple within the upper and lower limits of  
the Sidebands should be within 2.7 db. Maintain input  
level at  $78 \pm 0.5$  mv.

T. Test is now completed.

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## VI FREQUENCY SHIFT ASSEMBLY 4079993-2 (A5795)

### A. PRELIMINARY SET UP

1. Connect +24 volt power supply to +24v A0121 terminals.
2. Set +24 volt power supply to ON, and allow the A0121, 3.0 MHZ frequency standard at least 30 minutes of warm up time before proceeding with test.

### B. SET FRONT PANEL CONTROLS AS FOLLOWS:

1. MODE - CW
2. TEST - FS
3. TMC - OFF (OUT)
4. LSB and USB LINE/MIKE controls set to mid point.
5. USB/LSB switch - USB (IN)
6. PTT - OFF (OUT)
7. PWR - OFF (OUT)

### C. SET TOP REAR PANEL CONTROLS AS FOLLOWS:

1. MON CNTL - FS/FAX
2. FS LOOP - CONT
3. SHIFT switch -  $\pm$  425
4. FAX ADJ - FULLY CCW
5. SENSE +
6. SHIFT pot - 0 (mid range)
7. CONT - UNKEYED

### D. INSERT FREQUENCY SHIFT ASSEMBLY 4079993-2 INTO J3 CONNECTOR

### E. SET PWR SWITCH TO ON (IN)



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F. MONITOR THE MON OUT JACK AND CHECK FOR THE FOLLOWING:

1. FREQUENCY - 3.0 MHZ
2. Verify level is adjustable to .4v P.P. (adjusted by R8)
3. Readjust to .4v P.P. (TRUE LEVEL AS DESIRED IS DETERMINED IN ACTUAL SYSTEM) Record results on data sheet.
4. Set the MODE switch to FSK and adjust output level to .07 volts P.P. (ADJUSTED BY R48)

NOTE: The frequency and voltage levels desired are based upon the original board which was used as a standard. If during subsequent tests the recordings deviate out of spec., the original board must be retested and new parameters established. Frequencies are given as reference only. Careful attention is given to the D.C. voltages required.

G. MONITOR TERMINAL 10 FOR A LEVEL OF +7.47 VOLTS (ADJUSTED BY R38).

H. MONITOR TERMINAL H AND PERFORM THE FOLLOWING ADJUSTMENTS.

1. Adjust R36 for a level of +16.76 volts with the CONT sw off.
2. Set the CONT sw to KEY and adjust R45 for a level of +15.94 volts. Repeat steps 1 & 2 until the proper adjustments are obtained.

Set the CONT sw to OFF.

I. OBSERVE THE FOLLOWING VOLTAGES AND FREQUENCIES AND RECORD RESULTS ON THE TEST DATA SHEET.

FSK MONITOR OUT FREQUENCY REF. ONLY	SHIFT POS	SENSE MODE	DC OUT (PIN 4) VOLTAGE <u>±</u> .05v
3,000,858	425	+	+9.29
3,000,764	425	-	+8.84
3,000,209	170	-	+6.55
3,000,275	170	+	+6.78
2,999,941	85	+	+5.54
2,999,914	85	-	+5.42



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- T. SET THE PWR SWITCH TO OFF (OUT) AND REMOVE THE FREQUENCY SHIFT ASSEMBLY 4079993-2 FROM J3 CONNECTOR.
- U. TEST IS NOW COMPLETED.

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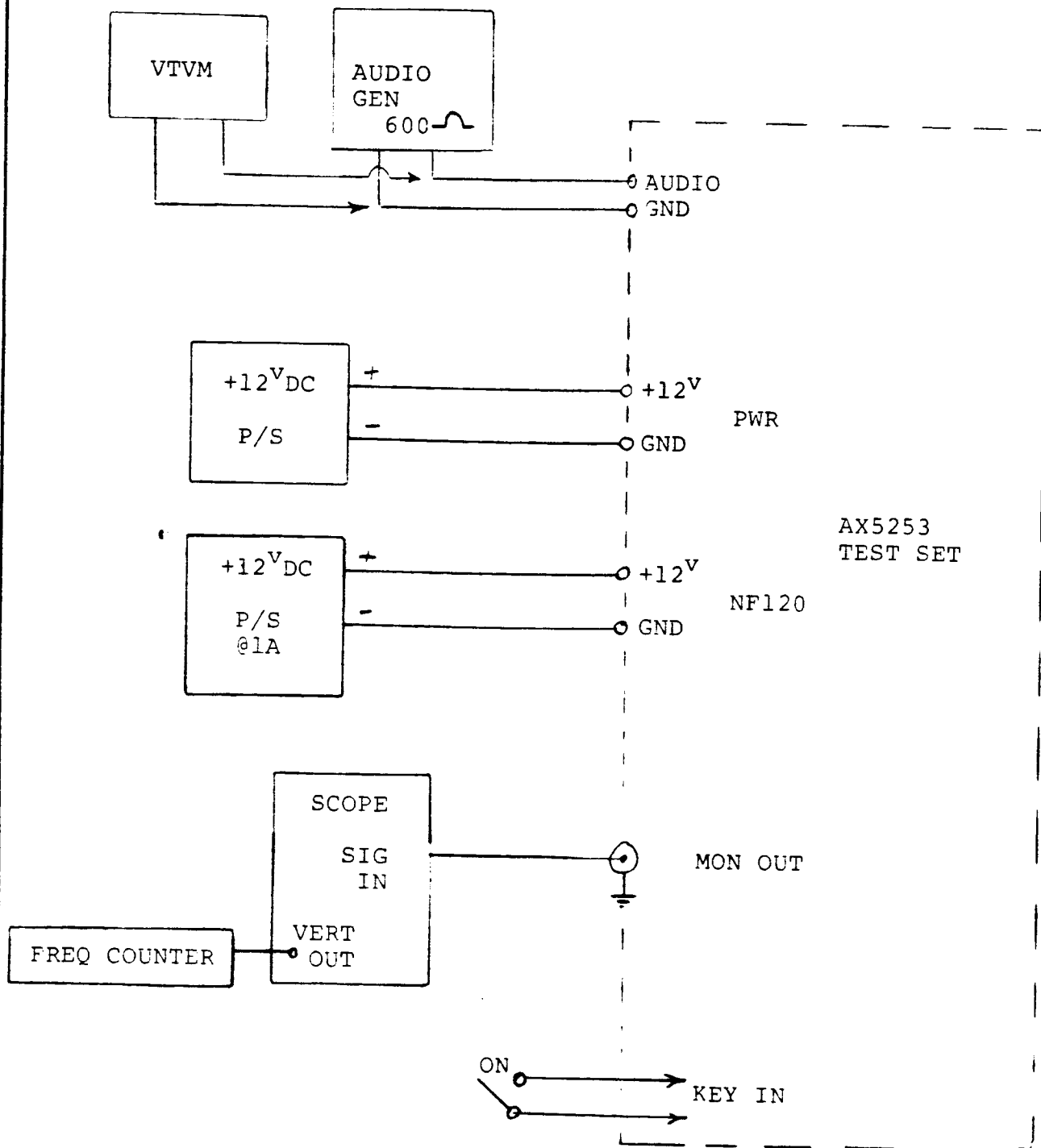
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FIGURE 1

TEST HOOK-UP FOR 4079994-1 (A5794 CARRIER GEN. ASS'Y)



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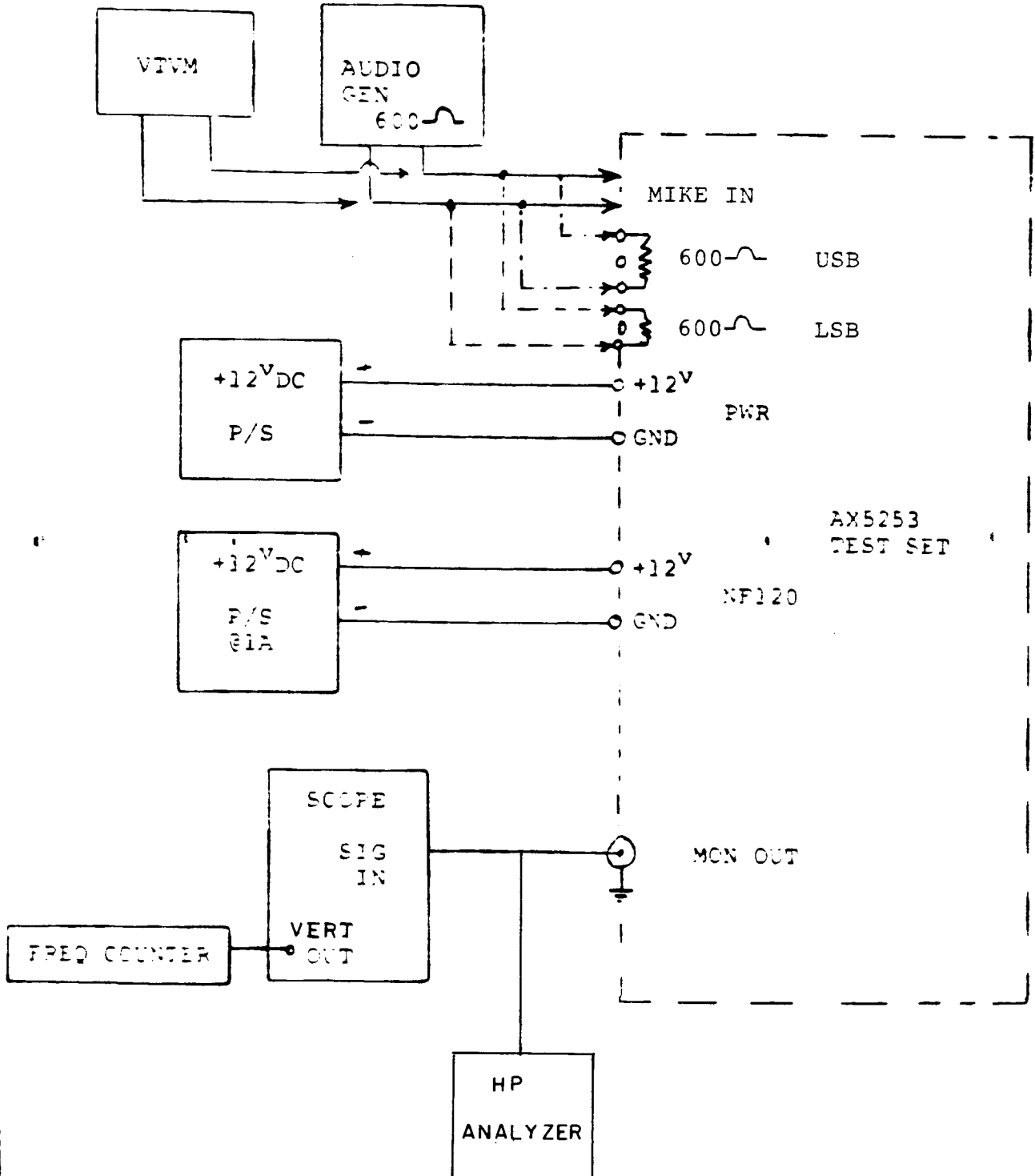
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FIGURE 2

TEST HOOK-UP FOR 4079992-1 (A5793 SIDEBAND ASS'Y)



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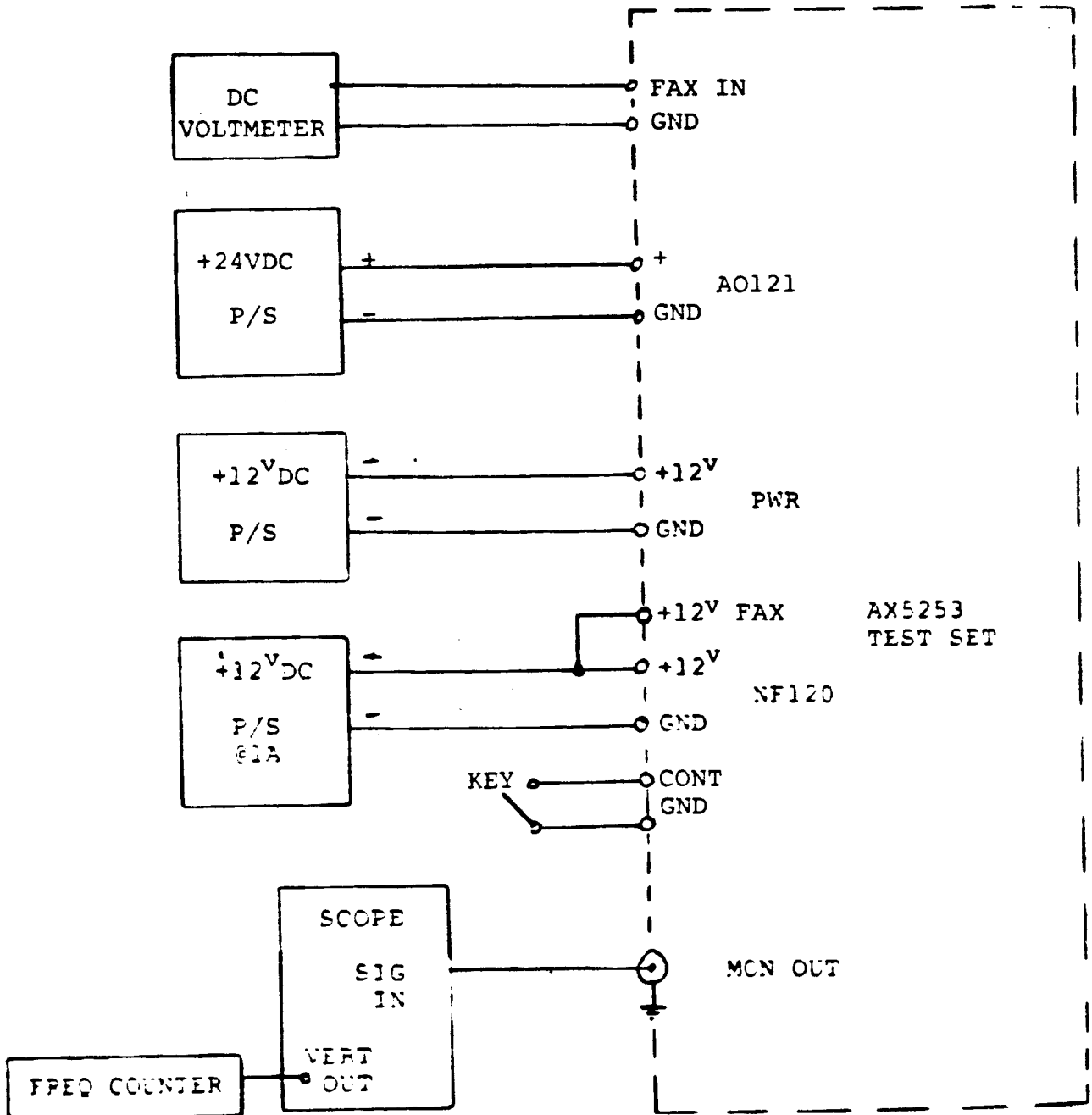
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FIGURE 3

TEST HOOK-UP FOR 4079993-2 (A5795 FSK ASS'Y)



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## TEST DATA SHEET

FREQUENCY SHIFT ASSEMBLY 4079993-2 (A5795)

SERIAL NO. \_\_\_\_\_

SECTION	STEP	TEST	FREQUENCY	LEVEL	ACCEPT
VI	F	CW 3 MHz OUT FSK	3.0 MHz	.4vP.P. .07vP.P.	_____ _____
	I	FSK OUT			

SHIFT POS	SENSE POS	DC OUT	+.05v
425	+	+9.29	_____
425	-	+8.84	_____
170	-	+6.55	_____
170	+	+6.78	_____
85	+	+5.54	_____
85	-	+5.42	_____
42.5	-	+4.86	_____
42.5	+	+4.92	_____

Q FAX OUT

FAX DC INPUT	DC OUT PIN 13	+.05V
+1V	+5.32V	_____
+2V	+5.05V	_____
+3V	+4.80V	_____
+4V	+4.53V	_____
+5V	+4.28V	_____
+6V	+4.01V	_____
+7V	+3.75V	_____
+8V	+3.49V	_____
+9V	+3.23V	_____
+10V	+2.97V	_____

S CONT KEY \_\_\_\_\_

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## TEST DATA SHEET (CONT)

NOTE: ALL TEST EQUIPMENT MUST BE CALIBRATED DUE DATE CAL. DATE ACCEPT.

II

- A. OSCILLOSCOPE-H.P. TYPE 541 OR EQUIVALENT \_\_\_\_\_  
TMC ID NO. \_\_\_\_\_
- B. +12vD.C. POWER SUPPLIES AS REQUIRED \_\_\_\_\_  
TMC ID NO. \_\_\_\_\_
- C. +24vD.C. POWER SUPPLIES AS REQUIRED \_\_\_\_\_  
TMC ID NO. \_\_\_\_\_
- D. FREQUENCY COUNTER-H.P. 524L OR EQUIVALENT \_\_\_\_\_  
TMC ID NO. \_\_\_\_\_
- E. AX5253 TEST SET      DATE \_\_\_\_\_ (1 YEAR CYCLE)
- F. D.C. VOLTMETER FLUKE MODEL 8020 B OR EQUIV. \_\_\_\_\_

TESTED BY \_\_\_\_\_ DATE \_\_\_\_\_

ACCEPTED BY \_\_\_\_\_ DATE \_\_\_\_\_

NCR - NO CALIBRATION REQUIRED



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## TEST DATA SHEET

CARRIER ASSEMBLY 4079994-1 (A5794)

SERIAL NO. \_\_\_\_\_

SECTION	STEP	TEST	FREQUENCY	LEVEL	ACCEPT
IV	D.	2.75 MHZ OUTPUT	2.75 MHZ	30 mV	_____
	G.	250 KHZ CW KEYED	250 KHZ	30 mV	_____
	I.	250 KHZ OUTPUT	250 KHZ	30 mV	_____
	L.	250 KHZ AM SIGNAL	250 KHZ	30 mV	_____
			1 KHZ		

NOTE: ALL TEST EQUIPMENT MUST BE CALIBRATED DUE DATE CAL. DATE ACCEPT.  
 II

- A. OSCILLOSCOPE-H.P. TYPE 541 OR EQUIVALENT  
 TMC ID NO. \_\_\_\_\_
- B. +12<sup>V</sup>D.C. POWER SUPPLIES AS REQUIRED  
 TMC ID NO. \_\_\_\_\_
- C. FREQUENCY COUNTER-H.P. 5244L OR EQUIVALENT  
 TMC ID NO. \_\_\_\_\_
- D. AUDIO GENERATOR-H.P. 200CD OR EQUIVALENT  
 TMC ID NO. \_\_\_\_\_
- E. VTVM-BALLANTINE 314 OR EQUIVALENT  
 TMC ID NO. \_\_\_\_\_
- F. AX5253 TEST SET      DATE \_\_\_\_\_ (1 YEAR CYCLE)

TESTED BY \_\_\_\_\_ DATE \_\_\_\_\_

ACCEPTED BY \_\_\_\_\_ DATE \_\_\_\_\_

NCR - NO CALIBRATION REQUIRED



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## TEST DATA SHEET

SIDEBAND ASSEMBLY 4079992-1 (A5793)

USB FX # 270 \_\_\_\_\_

LSB FX # 269 \_\_\_\_\_

Board Serial # \_\_\_\_\_

USB

Total Ripple

Frequency High \_\_\_\_\_

\_\_\_\_\_

Frequency Low \_\_\_\_\_

\_\_\_\_\_

LSB

Frequency High \_\_\_\_\_

\_\_\_\_\_

Frequency Low \_\_\_\_\_

\_\_\_\_\_

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4079994-1 Serial No. \_\_\_\_\_

## I LOG OF COMPONENT FAILURES

COMPONENT	USED ON	REASON	NOTES

## II LOG OF FAILURES NOT COMPONENT RELATED

TYPE OF FAILURE	USED ON	REASON	CORRECTIVE ACTION

