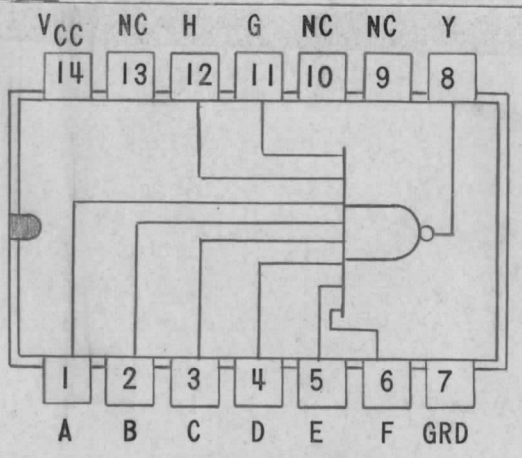
APPLICATION			REVISIONS						
QTY	MODEL USED ON	ASS'Y NO.	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD	APPD
	HFRR-4		X	EXPERIMENTAL RELEASE	3/7/68		C.V.		
			Ø	ORIG. RELEASE FOR PROD.	10-22-68	Ø	R.G.		
			A	DIM. OUTLINE ADDED	3/26/69	19351	JA		



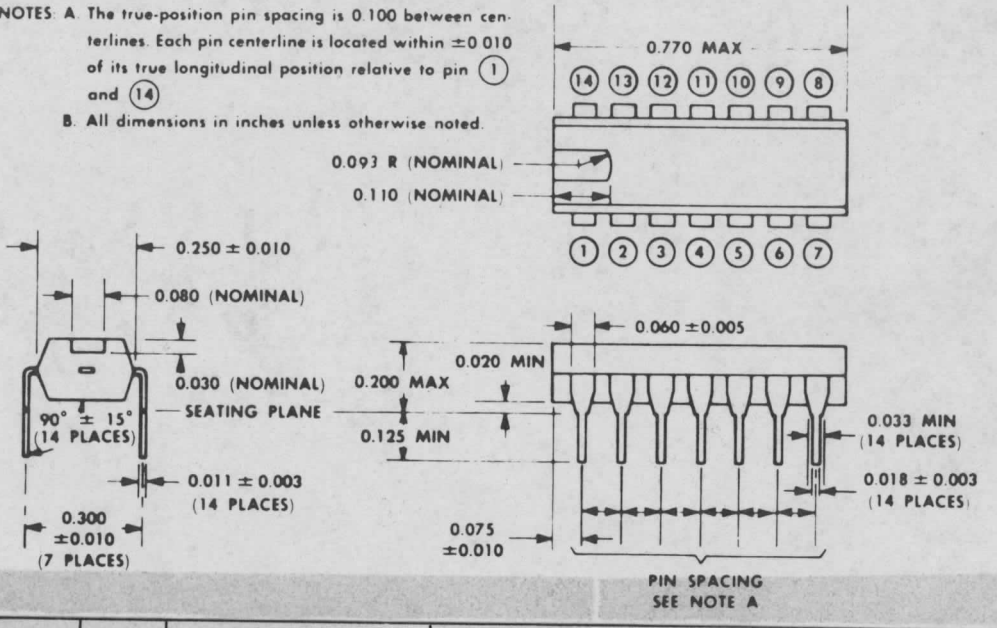
POSITIVE LOGIC
Y= ABCDEFGH

RECOMMENDED OPERATING CONDITION

SUPPLY VOLTAGE V_{CC} - 4.75V TO 5.25V
FAN-OUT FROM OUTPUT, N - 1 TO 10
ELECTRICAL CHARACTERISTICS, T_A = 0°C TO 70°C

NOTES: A. The true-position pin spacing is 0.100 between centerlines. Each pin centerline is located within ±0.010 of its true longitudinal position relative to pin (1) and (14)

B. All dimensions in inches unless otherwise noted.



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYM.
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005 TOLS. FRACTIONS 1/64 ANGLES 0° 30'	HOGAN			LIST OF MATERIAL	
MATERIAL	FINAL APPROVAL		DATE	THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK	
FINISH	MECH. DES.		DATE		
S401-265 (SN7430N)	ELECT. DES.		DATE 10-22-68	NETWORK, 8-INPUT POSITIVE NANDGATE	
	CHECKED		DATE 10/17/68		
	DRAWN C.V.		DATE 3-7-68		

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SIZE	CODE IDENT. NO.	DWG NO.	ISSUE
A	82679	NW 164	A
SCALE			SHEET OF

FCI-10-170 (10-66)