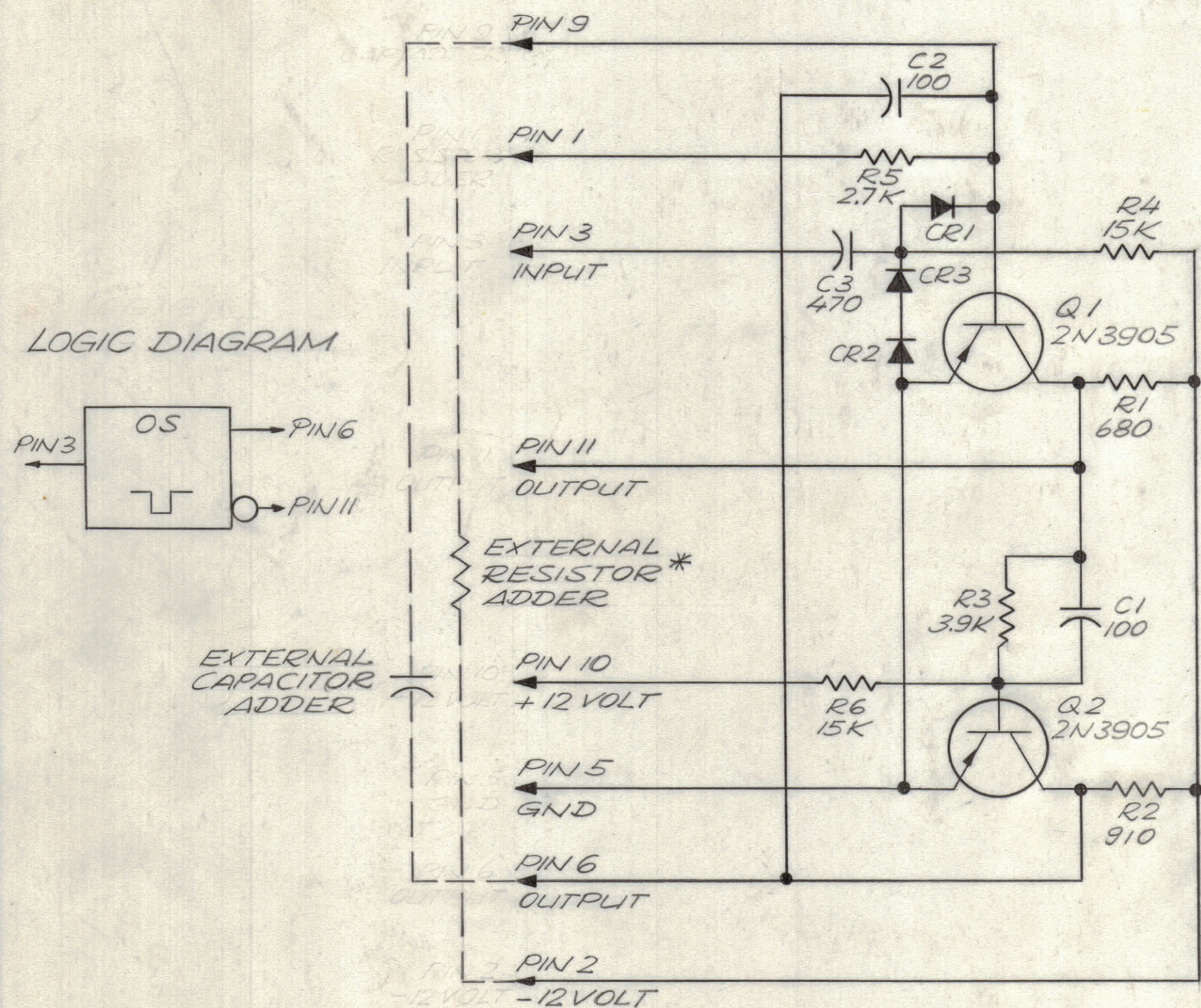


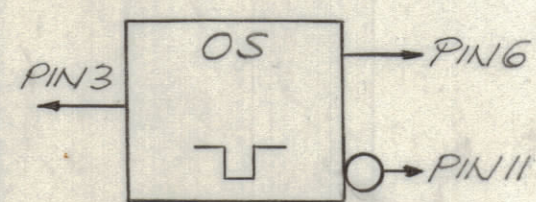
NW153

REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD APPD
	X	EXP. RELEASE	9/25/69		K.H.	
	X1	REV. & UPDATED	10/31/69		K.H.	
	X2	12 VOLT LOGIC TO ELEC. SPEC. ADD.	11/16/69		K.H.	
	Ø	ORIGINAL RELEASE FOR PRODUCTION	12/24/70		Ø	

SCHEMATIC DIAGRAM



LOGIC DIAGRAM

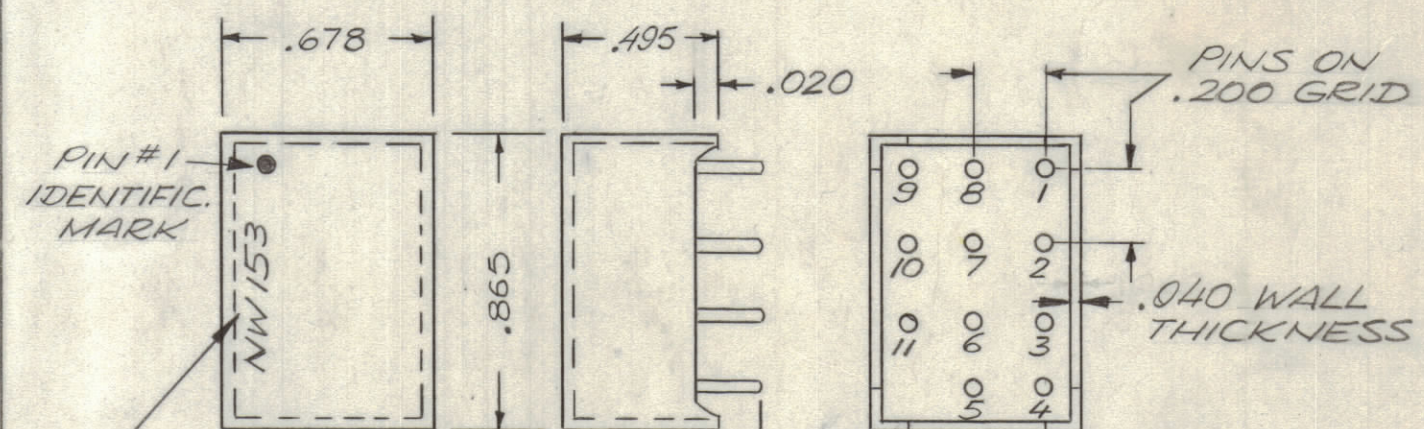


ELECTRICAL SPECIFICATIONS

12 VOLT LOGIC
 PIN # 5 GND
 PIN # 2 -12V
 PIN # 10 +12V
 OPERATING TEMPERATURE 0°C TO +75°C
 SWITCHING CHARACTERISTICS:
 INPUT MIN. VOLTAGE -5V
 OUTPUT MIN. VOLTAGE -10V
 INPUT $T_r = .2 \mu\text{sec.}$
 (PIN 11) OUTPUT $T_r = 1.0 \mu\text{sec.}$ $T_f = .4 \mu\text{sec.}$

MECHANICAL SPECIFICATIONS

POTTING COMPOUND: EPOTEC 509
 CASE MATERIAL: DIALLYL PHTHALATE
 POLARIZING PIN: PIN # 12



MARKING TO BE APPROX. 1/8 HIGH WHITE GOTHIC EPOXY MARK AS REQ. : NW 153
 NOTE: PINS ARE EXTRUDED COMPONENT LEADS. (DO NOT USE TRANSISTOR LEADS FOR MTG. PINS)

UNLESS OTHERWISE SPECIFIED:
 ALL RESISTORS ARE IN OHMS 1/4 W ± 5%
 ALL DIODES ARE IN 914
 ALL CAPACITORS ARE IN pF
 * JUMPER IF NOT REQ.

QTY. REQ.	ITEM	PART NO.	DESCRIPTION	SYMBOL
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LIST OF MATERIAL

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	FINAL APPROVAL <i>S. De Marco</i>	DATE 2/24/70
TOLERANCES ON	MECH. DES. <i>R. W. Zapp</i>	DATE 2-24-70
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005	ELECT. DES.	DATE
FRACTIONS ± 1/64 ANGLES ± 0° -30'	CHECKED <i>M. De Marco</i>	DATE 1/26/69
	DRAWN G.E. <i>K.H.</i>	DATE 3-26-69

THE TECHNICAL MATERIEL CORP.
 MAMARONECK, NEW YORK

NETWORK, DIGITAL
 SINGLE SHOT GENERATOR

QTY / UNIT	DDRR - 10M, K	ASS'Y NO.
APPLICATION		
CODE		

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SIZE	CODE IDENT. NO.	DWG NO.	ISSUE
C	82679	NW 153	Ø
SCALE			SHEET OF