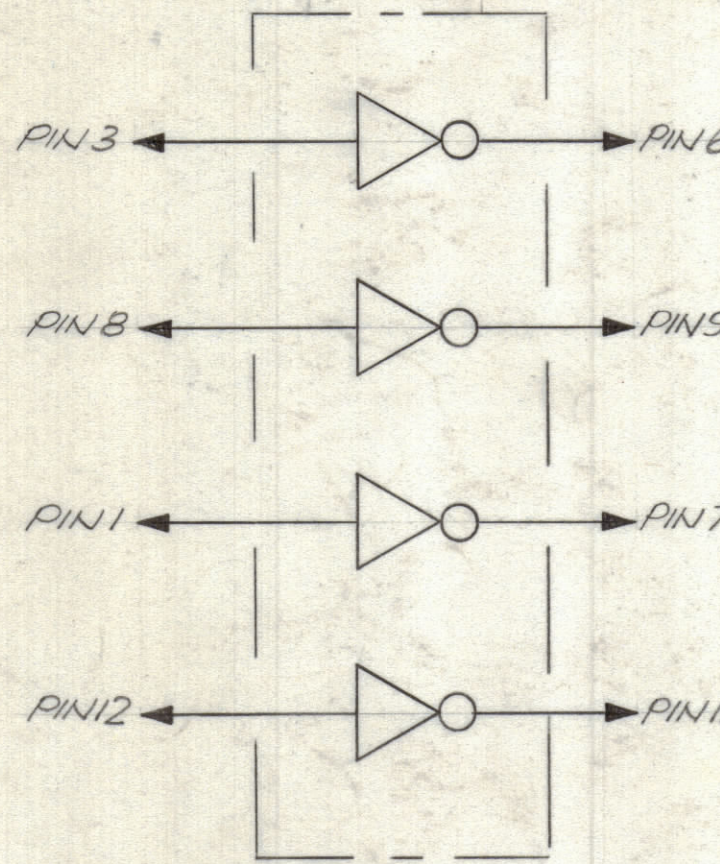
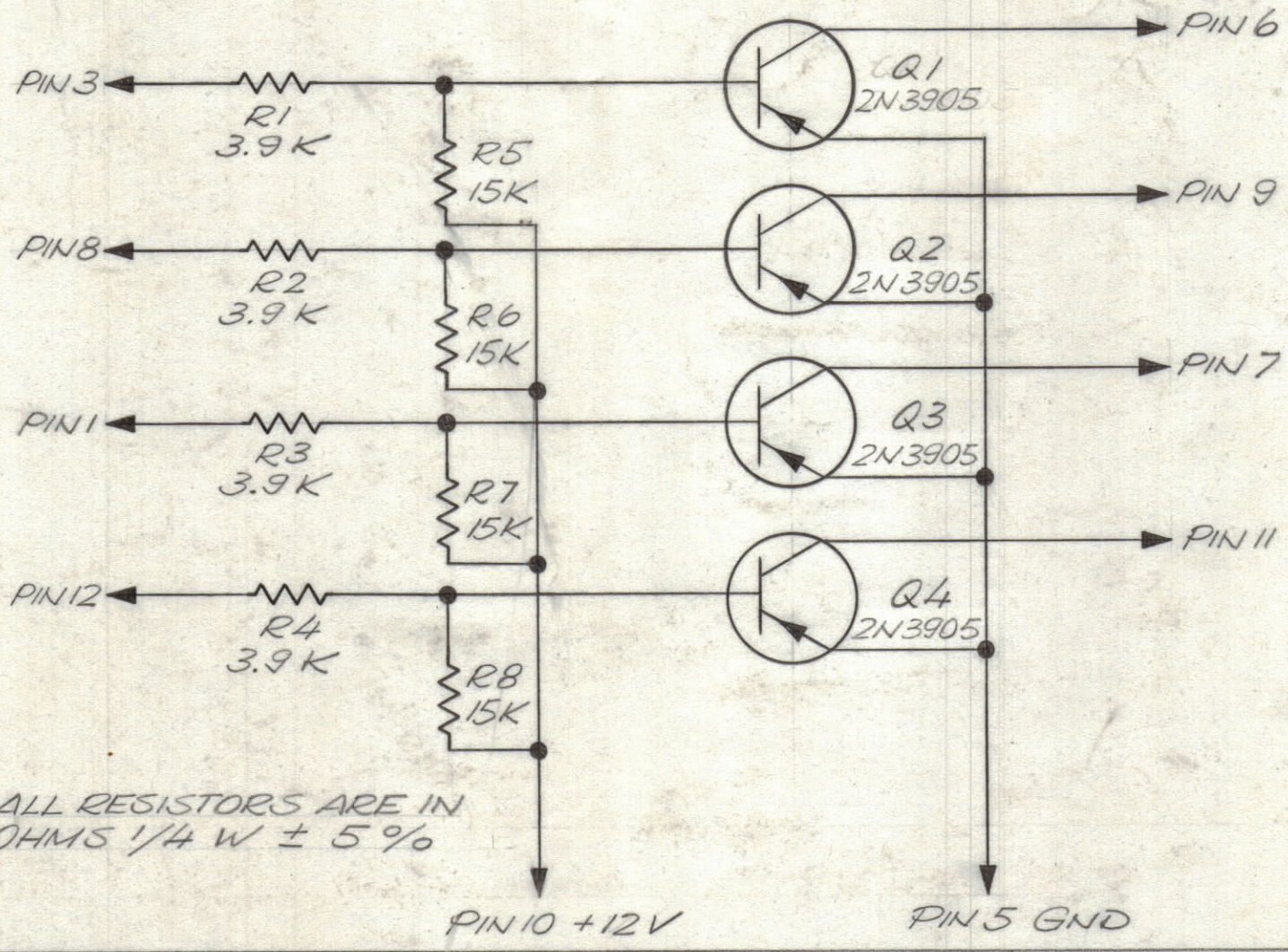


NW 150-4

LOGIC DIAGRAM



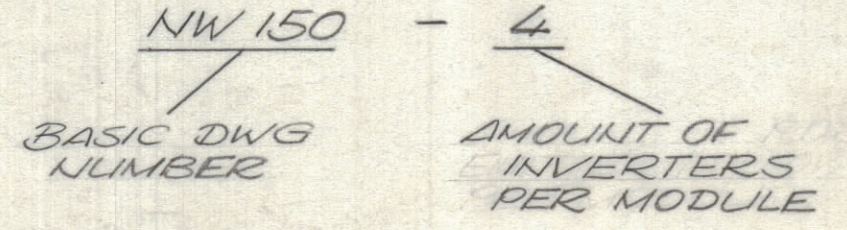
SCHEMATIC DIAGRAM



ALL RESISTORS ARE IN OHMS 1/4 W ± 5%

REVISIONS						
ZONE	LTR	DESCRIPTION	DATE	E.M.N.NO	DRAFT	CHKD APPD
	X	EXP. RELEASE	9/24/69		KH	
	X1	REV. & UPDATED	11/3/69		KH	
	X2	12 VOLT LOGIC TO ELEC. SPEC. ADD.	11/6/69		KH	
	Ø	ORIGINAL RELEASE FOR PRODUCTION	2/24/70		Ø	

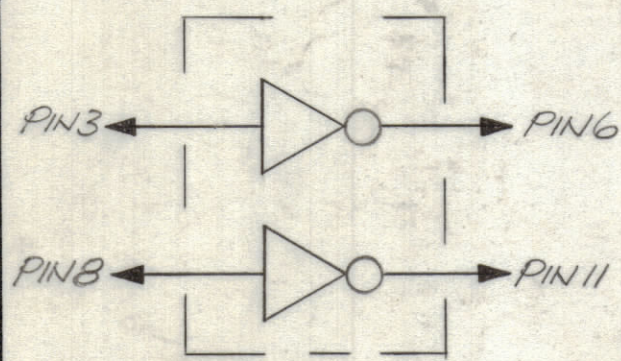
TMC PART NUMBER SHALL APPEAR IN THE FOLLOWING FORM:



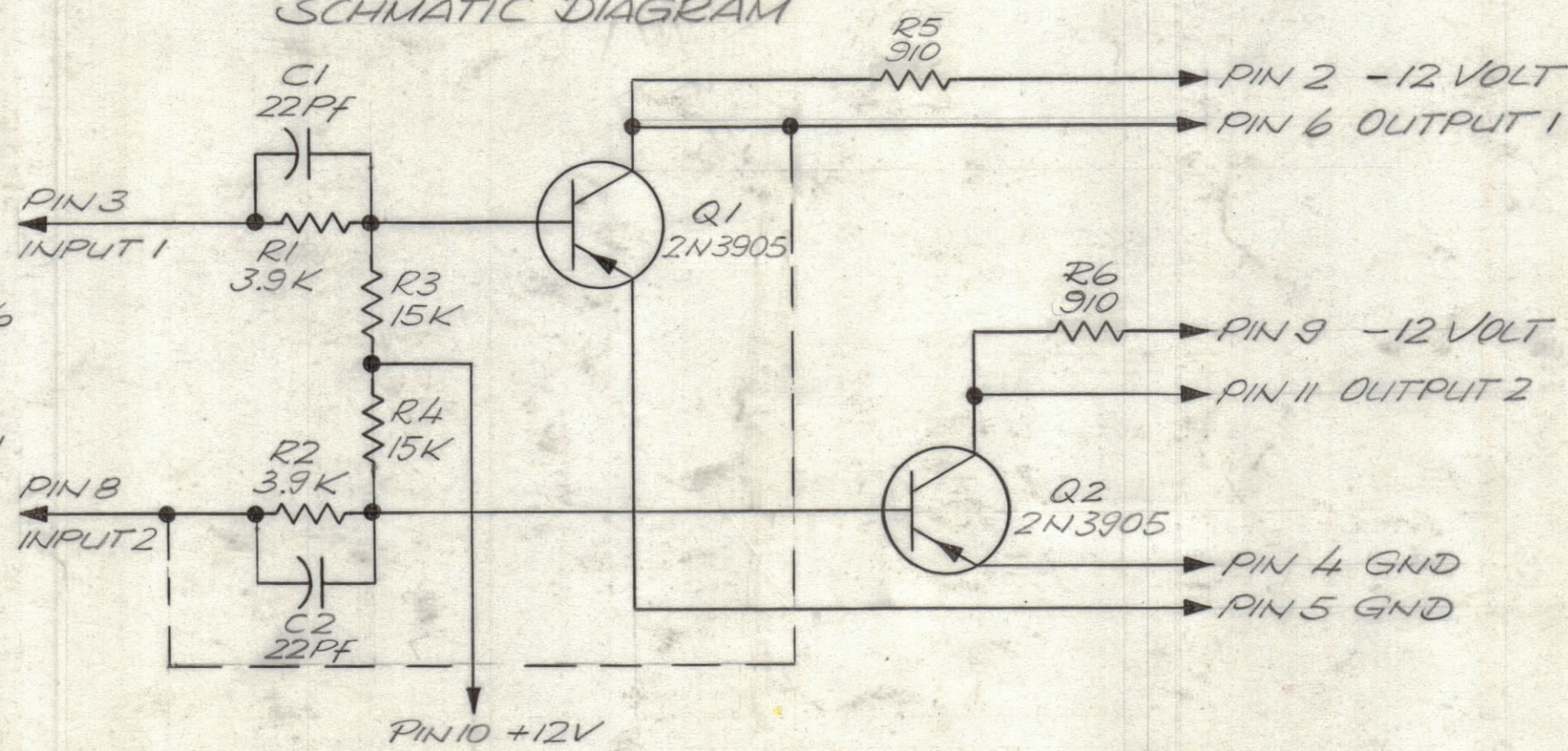
NW-150-2 PIN 5, 4 GND
 PIN 2, 9 - 12 V
 PIN 10 + 12 V
 OPERATING TEMPERATURE 0°C TO +75°C
 SWITCHING CHARACTERISTICS:
 EACH INPUT MIN. VOLTAGE -6.0 VDC
 EACH OUTPUT MIN. VOLTAGE -10.0 VDC
 INPUT T_f = LESS THAN .1 µsec. T_f = .16 µsec.
 OUTPUT T_f = .12 µsec. T_f = .2 µsec.
 12 VOLT LOGIC
 MECHANICAL SPECIFICATIONS:
 POTTING COMPOUND: EPOTEC 509
 CASE MATERIAL: DIALYL PHTHALATE
 POLARIZING PIN: NW 150-4-PIN # 4
 NW 150-2-PIN # 12

NW 150-2

LOGIC DIAGRAM

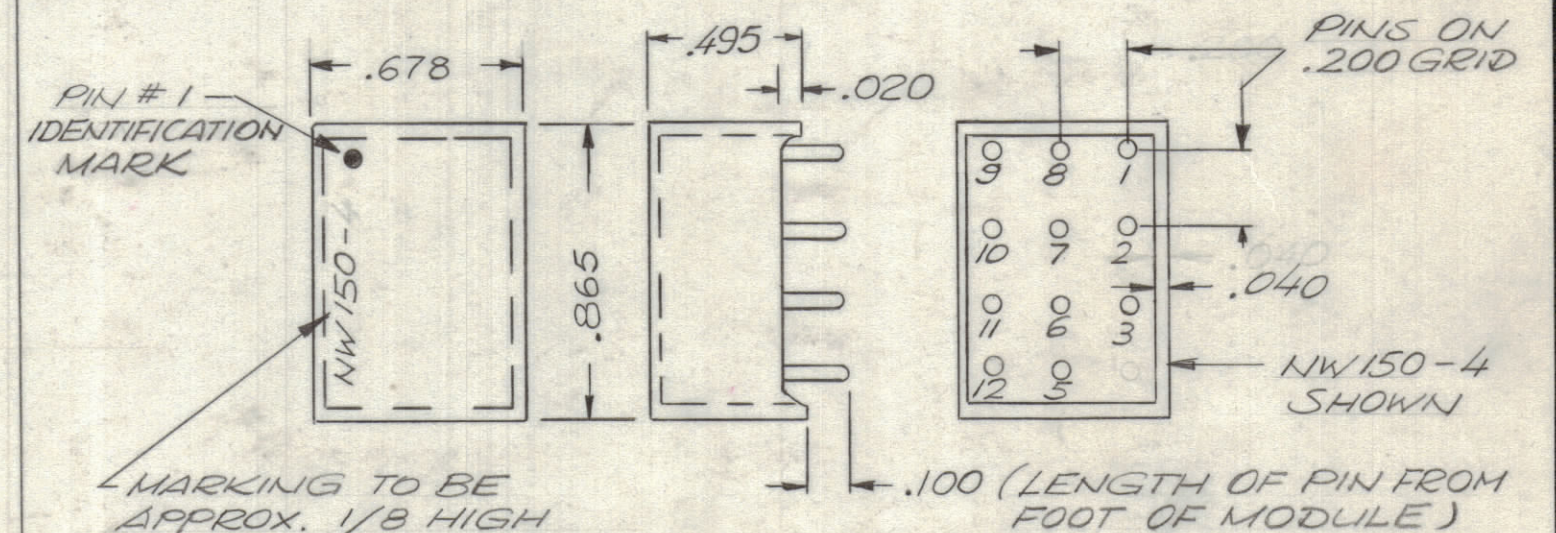


SCHEMATIC DIAGRAM



DASHED LINE INDICATES EXTERNAL CONNECTION TO BE MADE FOR USE AS A NON-INVERTING AMPLIFIER USE PIN 11 FOR OUTPUT.

ALL RESISTORS ARE IN OHMS 1/4 W ± 5%



MARK AS REQ.:
 NW 150-4
 NW 150-2

NOTE:
 PINS ARE EXTRUDED COMPONENT LEADS DO NOT USE TRANSISTOR LEADS FOR MTG. PINS.

QTY. REQ.	ITEM	PART NO.	DESCRIPTION	SYMBOL
-----------	------	----------	-------------	--------

LIST OF MATERIAL

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	FINAL APPROVAL <i>[Signature]</i>	DATE 2/24/70
TOLERANCES ON	MECH. DES. <i>[Signature]</i>	DATE 2-24-70
DECIMALS .X ± .05 .XX ± .01 .XXX ± .005	ELECT. DES.	DATE
FRACTIONS ± 1/64 ANGLES ± 0° -30'	CHECKED <i>[Signature]</i>	DATE 9/24/69
	DRAWN G. EVANGELIST	DATE 3/21/69

THE TECHNICAL MATERIEL CORP.
 MAMARONECK, NEW YORK

NETWORK DIGITAL
 INVERTER

QTY / UNIT	DDRR - 10M, K	ASS'Y NO.
APPLICATION		
CODE		

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SIZE	CODE IDENT. NO.	DWG NO.	ISSUE
C	82679	NW150	Ø
SCALE	SHEET		OF