

REV	DATE	DESCRIPTION	BY	CHKD
1	2/2/52	INITIAL DESIGN	K.P.	
2	2/2/52	REVISED	K.P.	
3	2/2/52	REVISED	K.P.	
4	2/2/52	REVISED	K.P.	
5	2/2/52	REVISED	K.P.	
6	2/2/52	REVISED	K.P.	
7	2/2/52	REVISED	K.P.	
8	2/2/52	REVISED	K.P.	
9	2/2/52	REVISED	K.P.	
10	2/2/52	REVISED	K.P.	
11	2/2/52	REVISED	K.P.	
12	2/2/52	REVISED	K.P.	
13	2/2/52	REVISED	K.P.	
14	2/2/52	REVISED	K.P.	
15	2/2/52	REVISED	K.P.	
16	2/2/52	REVISED	K.P.	
17	2/2/52	REVISED	K.P.	
18	2/2/52	REVISED	K.P.	
19	2/2/52	REVISED	K.P.	
20	2/2/52	REVISED	K.P.	

THESE CONNECTIONS ARE OPERATION ABOVE 8.0 MC ONLY.

JUMPS WILL BE ACCORDING TO CUSTOM REQUIREMENTS.

USE C.T. ON COIL

REC. ANT. 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

ALDC TO LPA

POWER

REC. ANT.

REC. ANT.

REC. ANT.

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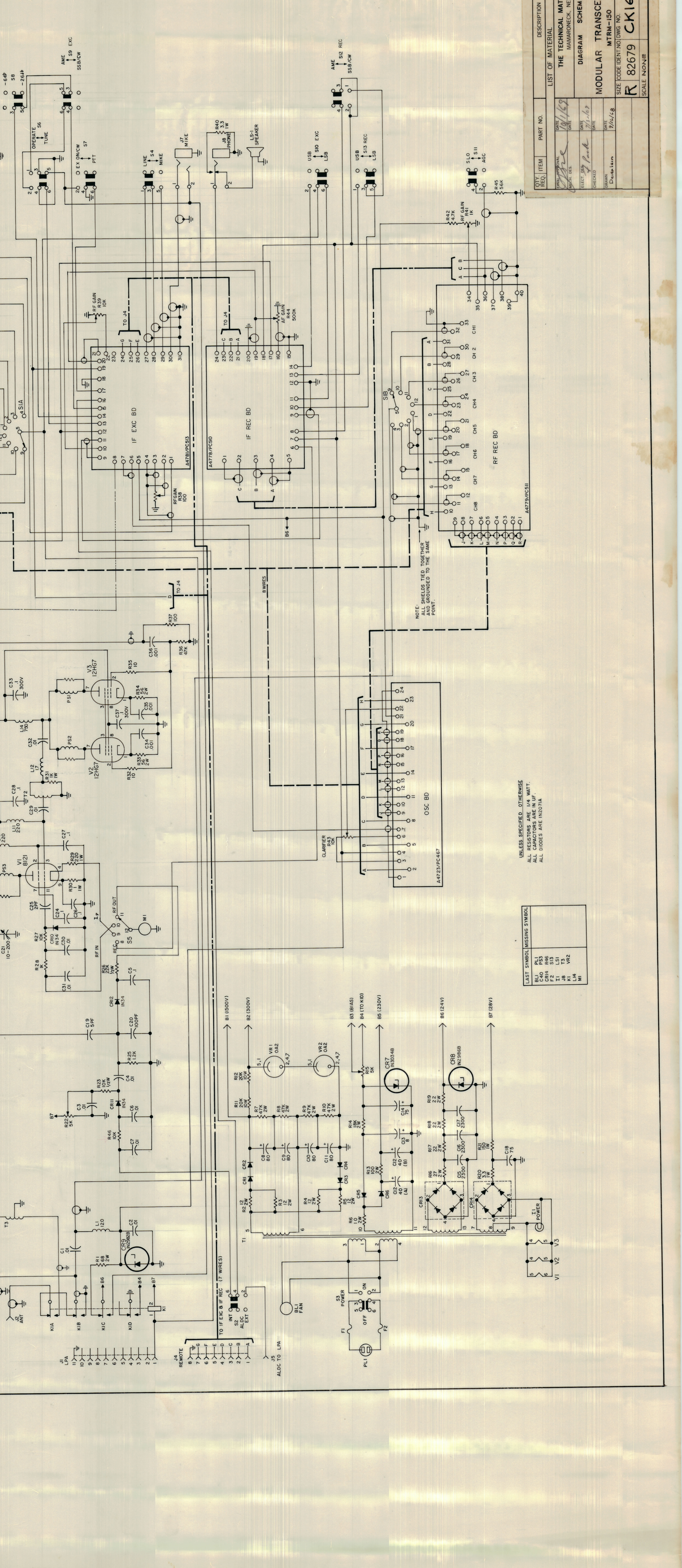
REC. ANT.

REC. ANT.

REC. ANT.

REC. ANT.

REC. ANT.



UNLESS SPECIFIED OTHERWISE ALL CAPACITORS ARE IN μF. ALL DIODES ARE IN 507A.

LAST SYMBOL MISSING SYMBOL

BU	BU
PS	PS
CH	CH
FO	FO
TI	TI
LS	LS
MI	MI
VR	VR
LA	LA
MI	MI

NOTE: ALL SHIELDS TIED TOGETHER AND GROUND TO THE SAME POINT.

8 WIRES

TO IF EXC. B IF REC. (7 WIRES)

REC. ANT.

REC. ANT.

DESCRIPTION: THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK

DIAGRAM SCHEMATIC

MODULAR TRANSCEIVER ASSY

MTRM-150

SIZE CODE IDENT NO/DWG NO.

R 82679 CK1662

SCALE NONE

ISSUE

SHEET OF